

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

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| | Bhupendra Sharma, et al. | Docket No.: TI-38242 |
| Serial No: | 10/711,532 | Art Unit: 2816 |
| Filed: | September 24, 2004 | Examiner: Anh Quan Tra |
| Customer No.: | 23494 | Conf. No.: 5531 |
| Title: | Avoiding Excessive Cross-Terminal Voltages of Low Voltage Transistors Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages | |

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT PURSUANT TO 37 CFR 1.111

Dear Sir:

This amendment is in response to the Office Action dated 2/14/2006, having a statutory period for response set to expire on 5/14/2006.

Amendments to Claims begin on page 2.

Remarks/Arguments begin on page .